

PART B - FEE(S) TRANSMITTAL

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23125 7590 11/04/2005

FREESCALE SEMICONDUCTOR, INC.
LAW DEPARTMENT
7700 WEST PARMER LANE MD:TX32/PL02
AUSTIN, TX 78729

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I hereby certify that this Fee(s) Transmittal is being deposited with the United States Postal Service with sufficient postage for first class mail in an envelope addressed to the Mail Stop ISSUE FEE address above, or being facsimile transmitted to the USPTO (571) 273-2885, on the date indicated below.

Stacie Herrera (Depositor's name)
Stacie Herrera (Signature)
11-29-05 (Date)

11/30/2005 TBESHAH2 00000046 503079 10657304

01 FC:1501 1400.00 DA
02 FC:1504 300.00 DA

| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|-----------------|-------------|----------------------|---------------------|------------------|
| 10/657,304 | 09/08/2003 | Kiran K. Gullapalli | SC12003TS | 9732 |

TITLE OF INVENTION: METHOD AND APPARATUS FOR DISTORTION ANALYSIS IN NONLINEAR CIRCUITS

| APPLN. TYPE | SMALL ENTITY | ISSUE FEE | PUBLICATION FEE | TOTAL FEE(S) DUE | DATE DUE |
|----------------|--------------|-----------|-----------------|------------------|------------|
| nonprovisional | NO | \$1400 | \$300 | \$1700 | 02/06/2006 |

| EXAMINER | ART UNIT | CLASS-SUBCLASS |
|---------------|----------|----------------|
| LEVIN, NAUM B | 2825 | 716-005000 |

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

- ☐ Change of correspondence address (or Change of Correspondence Address Form PTO/SB/122) attached.
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2. For printing on the patent front page, list

- (1) the names of up to 3 registered patent attorneys or agents OR, alternatively,
(2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

1. Joanna G. Chiu
2. James L. Clingan, Jr.
3.

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

Freescale Semiconductor, Inc.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Austin, Texas

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

4a. The following fee(s) are enclosed:

- ☒ Issue Fee
☒ Publication Fee (No small entity discount permitted)
☐ Advance Order - # of Copies

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5. Change in Entity Status (from status indicated above)

- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

The Director of the USPTO is requested to apply the Issue Fee and Publication Fee (if any) or to re-apply any previously paid issue fee to the application identified above. NOTE: The Issue Fee and Publication Fee (if required) will not be accepted from anyone other than the applicant; a registered attorney or agent; or the assignee or other party in interest as shown by the records of the United States Patent and Trademark Office.

Authorized Signature

Ranjeev Singh

Date

11/29/05

Typed or printed name

Ranjeev Singh

Registration No.

47,093

This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

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LAW DEPARTMENT
FREESCALE SEMICONDUCTOR, INC.

DATE: November 29, 2005

TO: MS: ISSUE FEE (571) 272-2885
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USPTO (571) 273-2885
(LOCATION) (FAX NUMBER)

FROM: Stacie Herrera for Ranjeev Singh (512) 996-6848
(SENDER) (EXTENSION)

TOTAL NUMBER OF PAGES 3 (including this page)IF YOU HAVE ANY TROUBLE OR QUESTIONS WITH TRANSMISSION, OR HAVE RECEIVED IT IN
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Docket No.: SC12003TS
Applicant: Kiran K. Gullapalli
Serial No.: 10/657,304
Art Unit: 2825
Filed: September 8, 2003

ALL ITEMS MARKED WITH AN "X" ARE INCLUDED:

| | | |
|----|---|--|
| 1. | x | 1 page Facsimile Cover Sheet |
| 2. | x | 1 page PTOL-85B Issue Fee Transmittal (in duplicate) |

Paid by Deposit Account 503079, Freescale Semiconductor, Inc: \$1700

If Applicant has overlooked any additional fees, or if any overpayment has been made, the Commissioner is hereby
authorized to credit or debit Deposit Account 503079, Freescale Semiconductor, Inc.I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS
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Date
SignatureFREESCALE LAW DEPARTMENT
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AUSTIN, TEXAS 78729
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